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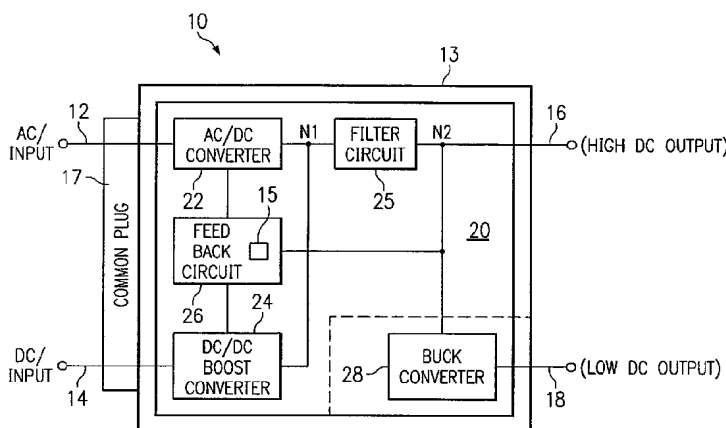
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(54) Title: DUAL INPUT AC AND DC POWER SUPPLY HAVING A PROGRAMMABLE DC OUTPUT UTILIZING A SECONDARY BUCK CONVERTER



(57) Abstract: A dual input AC/DC power converter (10) having dual inputs (12, 14) adapted to receive both an AC and DC input and provide a selectable DC voltage output (16) and a second DC output (18). The dual input AC/DC power converter (10) comprises a power converter circuit (20) having an AC-to-DC converter (22), a DC-to-DC booster converter (24), a feedback circuit (26), a filter circuit (25) and a DC-to-DC buck converter (28). Advantageously, the power converter (10) resolves many of system management problems associated with carrying all of the different interface components necessary to power a wide variety of mobile products from either an AC or DC power supply. In addition, the power converter (10) also advantageously includes dual output voltage terminals (16/18) to allow for multiple mobile devices of varying power requirements to be powered simultaneously, by a single converter.



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**DUAL INPUT AC and DC POWER SUPPLY HAVING A
PROGRAMMABLE DC OUTPUT UTILIZING A SECONDARY BUCK
CONVERTER**

5 CROSS REFERENCE TO RELATED APPLICATIONS

The present application is related to and claims priority from commonly assigned U.S. Patent application Serial No. 10/005,961 filed December 3, 2001, the teachings of which are incorporated herein by reference.

10 TECHNICAL FIELD

The present invention generally relates to the field of power converters, and more particularly to a dual input AC and DC to programmable DC output power converter.

BACKGROUND OF THE INVENTION

15 As the use of mobile electronic products, such as PC notebooks, PDAs , cellular telephones and the like, continues to increase, the need for low cost, compact power supplies to power and recharge these products also continues to increase. Most manufacturers of mobile products typically include plug-in power adapters along with these mobile products to help facilitate the power supply
20 needs of their customers.

Today's power adapters are typically AC-to-DC, or DC-to-DC power converters which are configured to either step-up or step-down the DC voltage input delivered to the mobile device. With AC-to-DC adapters, for example, users can power most mobile devices by simply plugging the adapter into a simple AC
25 wall outlet commonly found in most homes or offices. Similarly, when only DC

input power is available, such as in an automobile or airplane, users can still power their mobile devices by simply using a standard, off-the-shelf DC-to-DC adapter. Normally, both adapters are designed and tailored to provide a regulated DC output voltage, which typically range from between 5VDC to 30VDC
5 depending on the kind of mobile device being powered.

Although these power adapters conveniently provide direct power and recharging capabilities, users are often required to carry separate adapters to provide power to each individual mobile device. This often means that users have to carry multiple adapters: one for an AC input power source, and another for a
10 DC input power source, moreover, users typically carry multiple adapters to power multiple devices. Thus, by carrying more than one device at a time, users of mobile product users are forced to carry more than one bulk power supply adapter.

Accordingly, there exists a need for a power converter that resolves the
15 system management problems associated with carrying all of the different power supply components necessary to power a wide variety of mobile or portable devices. Moreover, such a power converter would advantageously encompass serving the power supply needs of several different mobile devices, as it would supply a filtered and regulated DC output voltage in response to either an AC and
20 DC input voltage. Moreover, by having a power convert or having multiple output terminals, users have the ability of providing power to several mobile devices of varying power requirements, simultaneously, regardless of whether the input voltage is AC or DC.

25

SUMMARY OF THE INVENTION

The present invention achieves technical advantages as a power converter capable of supplying dual DC output voltages derived from either an AC input voltage or a DC input voltage. The power converter can be externally
5 programmable to cover a wide range of voltage and current combinations, suitable for a wide variety of mobile product offerings. Moreover, the power converter also resolves the management problems associated with having several different interface components necessary to power a wide variety of mobile products. By having dual output voltage connections, mobile product users can simultaneously
10 power multiple mobile devices of varying power specifications.

In one preferred embodiment, the invention is a power converter having a first circuit adapted to receive an AC input voltage and provide a first programmable DC output voltage. The power converter includes a second circuit adapted to provide a second programmable DC output voltage in response to a
15 DC input voltage. The power converter also includes a third circuit that, in response to receiving the first and second DC output voltages, generates a selectable DC output voltage at a first output. Moreover, the third circuit generally comprises a feedback circuit and is adapted to interface with a removable program module. This programming module feature allows users of the power converter to
20 selectively establish the voltage level of the DC output voltage. The power converter also includes a fourth circuit that is coupled to first output. The fourth circuit provides a second DC output voltage as a second output which is independent of, and substantially lower than the selectable DC output voltage.

25 In another embodiment, the invention is a method of generating at least two independently selectable DC output voltages in response to an AC input voltage or a DC input voltage. This method is achieved by the act of converting

the received AC or DC input voltage to a first programmable DC output voltage at a first output. The converting act is then followed by a receiving act wherein the first DC output voltage is received by a converting circuit. The converting circuit initiates a generating act generating a second DC output voltage that is

5 independent of and substantially lower than the programmable DC output voltage.

BRIEF DESCRIPTION OF THE DRAWINGS

Advantages of the invention and the specific embodiments will be understood by those of ordinary skill in the art by reference to the following detailed description of preferred embodiments taken in conjunction with the
5 drawings, in which:

Figure 1A shows a block diagram of a dual input AC and DC power converter having dual DC voltage outputs in accordance with the present invention;

Figure 1B shows an exploded view of the converter with the detachable
10 buck circuit;

Figure 2 shows a schematic diagram of the power converter circuit as illustrated in Figure 1 in accordance with the present invention; and

Figure 3 shows a detailed schematic diagram of a DC-to-DC buck converter circuit in accordance with the present invention; and

15

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

The numerous innovative teachings of the present applications will be described with particular reference to the presently preferred exemplary embodiments. However, it should be understood that this class of embodiments provides only a few examples of the many advantageous uses and innovative teachings herein. In general, statements made in the specification of the present application do not necessarily delimit any of the various claimed inventions. Moreover, some statements may apply to some inventive features, but not to others.

10 There is shown in Figure 1A a block diagram of a dual input AC/DC power converter 10 having dual programmable DC voltage outputs in accordance with the present invention. Preferably, the dual input AC/DC power converter 10 comprises a power converter circuit 20 having an AC-to-DC converter 22, a DC-to-DC booster converter 24, a feedback circuit 26, a filter circuit 25 and a DC-to-DC buck converter 28. The power converter circuit 20 is seen housed in housing 15 13 and advantageously provides a first programmable DC output voltage at DC output terminal 16 and a second programmable DC output voltage at terminal 18. Both of these DC output voltages may be generated as a function of both AC and DC input voltages.

20 In operation, the AC-to-DC converter 22 receives an AC signal via input terminal 12 and provides a regulated DC output voltage at node N1. Similarly, the DC-to-DC booster converter 24 may receive a DC input voltage at its input via input terminal 14 and may also provide a regulated DC output voltage at node N1.

25 Input terminals 12 and 14 are integrated into a single common connector 17 such that different power cords adapted to receive input power from different sources are received by the common connector 17. For instance, DC power from

an airplane or car power source are wired to couple to input 12 and AC source is wired to couple to input 14. In a selected embodiment, the AC-to-DC converter 22 is adapted to generate a DC output voltage of between 15VDC and 24VDC in response to an AC input voltage at terminal 12 ranging between 90VAC and 265VAC. Likewise, the DC-to-DC booster converter 24 is adapted to provide a DC output voltage which is substantially similar to that of converter 22, but which is generated in response to a DC input voltage supplied at input terminal 14. Preferably, DC-to-DC booster converter 24 is adapted to receive a voltage in the range of between 11VDC and 16VDC. Advantageously, AC-to-DC conversion, via AC-to-DC converter 22, allows users of the power converter 10 to power high-power mobile devices, such as a laptop computer wherever AC input power is available, such as in the home or office, for example. Conversely, the DC-to-DC booster converter 24 of the power converter 10 is capable of powering similar high-power devices by stepping up most low amplitude DC input signals, such as those found in automobile and/or airplane environments.

As shown, filter circuit 25 has its input tied to the respective outputs of the converter 22 and 24. In a preferred embodiment, the filter circuit is adapted to provide a filtered DC output voltage at second node N2, which, thereafter, feeds output terminal 16, at an output power of 75 watts, for example.

The single feedback circuit 26 is shown coupled to the output of filter circuit 25 at node N2. In a preferred embodiment, the feedback 26 circuit, through a single feedback loop, regulates the voltage level of the filtered DC output voltages generated by both converters 22 and 24. Additionally, the feedback circuit 26 is adapted to receive a removable programming module that allows mobile device users to provide a selectable DC output voltage at output 16 via node N2. The programming module comprises a key 15 comprising a resistor, wherein different associated values of the resistor establish different associated

DC output voltages at output 16. By allowing users to selectively change the voltage level of the filtered DC output voltage, the power converter 10 may be adapted to power a variety of different mobile electronic devices, having different associated power requirements. Moreover, the power converter's 10 programming
5 module may also be adapted to provide the additional function of output current limiting.

The DC-to-DC buck converter 28 has its input coupled at node N2, providing a second DC output voltage that is then fed to output terminal 18, having an output power of 10 watts, for example. Preferably, buck converter 28
10 discreetly steps down the filtered DC voltage and produces a second DC output voltage at a separate output terminal 18. In a selected embodiment, the buck converter 28 steps down the filtered DC output voltage to a range of about 3VDC and 15VDC. Advantageously, this second DC output voltage generated by
15 converter 28 is independent of, and substantially lower than the DC output voltage at terminal 16. This allows users of the present invention to power not only a high-power peripheral, such as a laptop computer, but also, a second, low-power peripheral, such as a cell phone, PDA, and the like. Moreover, the present invention allows for these peripherals to be powered simultaneously by a single
20 converter, regardless if the input voltage is AC or DC. The buck converter 28 is physically detachable from the main housing 13 as shown in Figure 1B, allowing different buck circuits providing different output voltages to be selectively attached to housing 13 and tap the DC output voltage from output terminal 18.

Referring now to Figure 2 there is shown a schematic diagram of the power converter circuit 20 of the dual input AC/DC power converter 10 as
25 depicted in Figure 1 in accordance with an exemplary embodiment of the present invention. As described herein in greater detail, the power converter circuit 20, in

a preferred embodiment, comprises three separate converters: AC-to-DC power converter 22, DC/DC boost converter 24, and DC-to-DC buck converter 28.

AC-TO-DC CONVERTER

The AC-to-DC power converter 22 includes a true off line switcher which
5 is configured in a fly-back topology. Full-wave rectification of an AC input
signal, received at input terminal 12, occurs using a full-wave bridge rectifier
BD1 and a filter capacitor C1, which creates a DC voltage bus from which the
switcher operates. Inductor L1 offers additional EMI filtering of the AC signal
after the signal has been rectified through the full-wave bridge. The AC-to-DC
10 converter 22 also includes a main controller IC1 configured as a current mode
pulse-width modulator (PWM). Main controller IC1 is also configured to have a
single-ended output with totem pole driver transistors coupled thereto. The AC-
to-DC power converter 22 has a main power switch Q1 which drives the main
transformer T1. In a preferred embodiment, the transformer T1, Schottky diode
15 D11, and filter capacitors C24 and C25 combine to provide the DC output voltage
at node N1.

As noted earlier, filter circuit 25 allows for additional filtering of the DC
output voltage derived from node N1. The filter circuit 25 itself comprises
inductor L3, capacitor C26 and transformer NF1. Advantageously, the filter
20 circuit 25 produces a filtered DC output voltage at output 16 having less than
100mv peak-to-peak noise and ripple.

The feedback circuit 26, through a single feedback loop, is capable of
regulating the filtered DC output voltages provided by the converters 22 and 24.
The feedback circuit 26 is also adapted to be coupled to a removable
25 programming module having a key 15, comprising resistor R53. As such, the

present invention allows users to selectively program the DC output voltage later received at output terminal 16. The feedback circuit 26 includes a photocoupler circuit comprising a pair of photocouplers PH1 and PH3 connected in series (i.e., stacked), each being coupled to the outputs of operational amplifiers IC4-A and IC4-B. Advantageously, these photocouplers are arranged along the feedback loop of the feedback circuit 26. Additionally, the feedback circuit 26 efficiently regulates the filtered DC output voltages generated by both converters 22 and 24 through a single feedback loop. In stacking the photo-couplers, the present invention also allows the power converter 10 to maintain proper input/output isolation between respective terminals 12 and 14 and output terminal 16.

Preferably, the output current limiting function of converter 22 is accomplished via integrated circuit IC4A, resistors R33, R37, R38, and R39 and programming resistor R54.

Over voltage protection of AC-to-DC converter 22 is achieved using photocoupler PH2 and zener diode ZD2. In a preferred embodiment, zener diode ZD2 is set at 25V such that when in avalanche mode it causes the transistor side of photocoupler PH2 to bias transistor Q1 into the on state. When it is the on state, transistor Q3 pulls low pin 1 of integrated controller IC1 and pulls the operating duty cycle of the integrated controller towards 0%. This takes the DC output voltage to 0 volts. Also, when transistor Q1 is on, transistor Q2 is also forced on which then forces these two transistors become latched. If transistors Q1 and Q2 are latched, input power must be recycled in order for the power converter 10 to be turned on again.

DC-TO-DC CONVERTER

The DC-to-DC converter 24 is configured in a boost topology and utilizes the same kind of integrated controller, IC2, as used in converter 22. In the DC-to-DC converter 24, transistor Q8 acts as the main power switch and diode D6 as the main rectifier. Preferably, inductor L2 is adapted to function as a power boost inductor, which is comprised of a toroid core-type inductor. It should be understood that the cathode leads of diodes D11 and D8 are connected, forming an ORed configuration, requiring only one output filter. Advantageously, this eliminates the board space needed for a second set of filters capacitors.

Like the AC-to-DC converter 22, the DC-to-DC converter 24 is also designed to operate at a frequency of around 80KHZ. For the AC-to-DC converter 22, the operating frequency is set by resistor R13 and capacitor C7. Likewise, the operating frequency of the DC-to-DC converter 24 are set by resistor R28 and capacitor C28.

The DC-to-DC converter 24 includes an over-voltage protection circuit comprising zener diode ZD2, resistor R23, R24, R48, transistor Q415, and silicon-controlled rectifier SC1. Zener diode ZD2 sets the over-voltage protection point (OVP) which is preferably set at 25VDC. Generally, there is no current flowing through resistor R48. If, however, when zener diode ZD2 begins to conduct current, the drop across R48 is significant enough to bias transistor Q6 on, pulling its collector terminal high, and thereby turning silicon controlled rectifier SC1 on. When silicon control rectifier SC1 is on, it pulls pin 1 of the integrated controller IC2 low. Thus, if pin 1 of integrated controller IC2 is low, the output drivers thereof are forced to operate at a duty cycle of 0%, thereby producing a DC output voltage of 0 volts at pin 6. Advantageously, the silicon controlled rectifier SC1 functions as a power latch circuit that requires that input

power be recycled in order to turn on the power converter 10 if a voltage above 25VDC is detected at node N1.

The temperature of the housing 13 of the power converter 10 is monitored using a thermistor NTC3. If, for example, there is a corresponding increase in the temperature of the housing 13, it will result in a decrease in the resistive value of thermistor NTC3, thereby causing transistor Q9 to turn on and pull low pin 1 of integrated circuit IC2 of converter 24. Moreover, this causes the photo-coupler PH2 to be biased enough to activate a latch circuit comprising transistors Q1 and Q2 that will shutdown the power converter 22. In addition, the power converter's thermal protection feature is adapted to operate regardless of whether an AC or DC input voltage is being received at their respective input terminals.

Figure 3 shows a detailed schematic diagram of the DC-to-DC buck converter 28 in accordance with the present invention. The buck converter 28 has an integrated circuit controller IC1, similar to converters 22 and 24, which is adapted to generate an on-time duty cycle to power transistor switch Q1. The operating frequency of controller IC1 is set by capacitor C6, which is coupled between pin 4 of IC1 and ground, and resistor R1, which is coupled between pins 4 and 8. In a selected embodiment, the diode D1 functions comprises a Schottky diode and functions as "catch" diode. Inductor L1 is a output power inductor and couples the gate of power transistor Q1 to V_{out} . Fuse F1 is shown coupled between V_{in} and the drain terminal of power transistor Q1, and advantageously provides current protection to buck-converter 28.

Furthermore, the input V_{in} of the buck converter 28 is coupled to the output of filter circuit 25 at node N2, wherein V_{in} receives the filtered DC output voltage therefrom. In a preferred embodiment, the buck converter 28 provides a second DC output voltage at V_{out} , coupled to output terminal 18. Advantageously,

the buck convert 28 discreetly steps down the filtered DC output voltage and provides a second DC output voltage at output terminal 18 which is independent of, and substantially lower than the DC output voltage at output terminal 16. Likewise, the DC output voltage of the buck converter 28 enables users low-
5 power peripherals, such as, a cell phones, a PDAs, and/or similar mobile devices. In a selected embodiment, the buck convert 28 may also be adapted to provide a DC output voltage at output terminal 18 ranging between 3VDC and 15VDC, selectively determined as a function of the chosen value of resistor R1 used in the particular buck converter 28, with a total power delivery of 10 watts, for example.
10 As previously mentioned, the buck converter 28 may be housed in a separate, detachable program module that enables users to selectively program the DC output voltage at terminal 18 as a function of different associated buck converter modules.

Though the invention has been described with respect to specific preferred
15 embodiments, many variations and modifications will become apparent to those skilled in the art upon reading the present application. It is therefore the intention that the appended claims be interpreted as broadly as possible in view of the prior art to include all such variations and modifications.

20

WHAT IS CLAIMED IS:

1. A power converter, comprising:
 - a first circuit converting an AC input voltage to a first predetermined DC output voltage;
 - 5 a second circuit converting a DC input voltage to a second predetermined DC output voltage;
 - a third circuit receiving said first and second predetermined DC voltages and, in response thereto, providing a selectable DC output voltage at a first output, wherein said selectable DC output voltage is established as a function
 - 10 a removable program module; and
 - a fourth circuit coupled to said first output and providing a second DC output voltage at a second output, whereby said second DC voltage output is independent of, and substantially lower than said selectable DC output voltage.
- 15 2. The power converter of Claim 1 further comprising a fifth circuit comprising a filter circuit adapted to filter said first and second predetermined DC output voltages and provide a respective first and second filtered DC output voltage at a common node.
- 20 3. The power converter of Claim 1 wherein said first circuit comprises a AC-to-DC flyback converter, wherein said AC-to-DC flyback converter is adapted to provide a DC output voltage of between 15VDC and 24VDC.
- 25 4. The power converter of Claim 1 wherein said second circuit comprises a DC-to-DC boost converter, wherein said DC-to-DC boost converter is adapted to provide a DC output voltage of between 15VDC and 24VDC.

5. The power converter of Claim 1 wherein said third circuit includes a feedback circuit coupled to said first circuit and said second circuit, said feedback circuit regulating the first and second predetermined DC voltages generated by said first and second circuits.

5

6. The power converter of Claim 5 wherein said feedback circuit comprises a single loop.

7. The power converter of Claim 1 wherein said fourth circuit
10 comprises a DC-to-DC buck converter providing said second DC output voltage, said DC-to-DC buck converter providing said second DC output voltage of between 3VDC and 15VDC.

8. The power converter of Claim 1 wherein said first and second
15 predetermined DC output voltages are substantially the same and are provided to a common node.

9. The power converter of Claim 1 wherein said first and second
predetermined DC output voltages of said respective first and second circuit are
20 established via said removable program module, wherein said removable program module comprises a key adapted to be removably coupled to said power converter.

10. The power converter of Claim 1 wherein said removable program
25 module comprises a key having a resistor, wherein said first and second DC output voltage are a function of the value of said resistor.

11. The power converter of Claim 10 wherein said key establishes an output voltage function.

12. The power converter of Claim 10 wherein said key establishes an
5 output current limiting function.

13. The power converter of Claim 1 wherein said first circuit is adapted to receive an AC input voltage having a range of 90VAC to 265VAC.

10 14. The power converter of Claim 1 wherein said second circuit is adapted to receive a DC input voltage having a range of 11VDC to 16VDC.

15 15. The power converter of Claim 1 wherein said first and second predetermined DC output voltages are programmable as a function of the program module.

16. The power converter of Claim 1 wherein said fourth circuit comprises a second removable program module, wherein said second DC output voltage at said second output is a function of said different associated second
20 removable program modules.

17. The power converter of Claim 1 wherein said fifth circuit further includes a protection circuit, said protection circuit provides an over-voltage protection function.

25

18. The power converter of Claim 1 wherein the first circuit and the second circuit receive their respective AC input voltage and DC input voltages at a common single connector.

19. A method of generating at least two independently selectable DC output voltages in response to either an AC input voltage or a DC input voltage, comprising the steps of:

5 converting the received AC or DC input voltage to a first
selectable DC output voltage;
receiving said first selectable DC output voltage and generating a
second DC output voltage which is independent of and substantially lower than
said first selectable DC output voltage.

10 20. The method of Claim 19 further comprising the step of selectively
establishing the voltage magnitude of said first selectable DC output voltage using
a removable program module.

15 21. The method of Claim 19 wherein said first selectable DC output
voltage is provided by an AC-to-DC flyback converter in response to an AC input
voltage.

20 22. The method of Claim 19 wherein said first selectable DC output
voltage is provided by a DC/DC boost converter in response to a DC input
voltage.

25 23. The method of Claim 19 further comprising the step of filtering
said first selectable DC output voltage via a filter circuit, said filter circuit
providing a filtered DC output voltage of between 15VDC and 25VDC.

24. The method of Claim 23 wherein said filter circuit output is coupled to a DC-to-DC buck converter, wherein said DC-to-DC buck converter is adapted to provide the second separate and independent DC output voltage of between 3VDC and 15VDC.

5

25. The method of Claim 19 wherein said removable program module comprises a key having a set of resistors, wherein said first selectable DC output voltage is a function of the value of one of said resistors.

10 26. The method of Claim 25 wherein said key establishes an output voltage function.

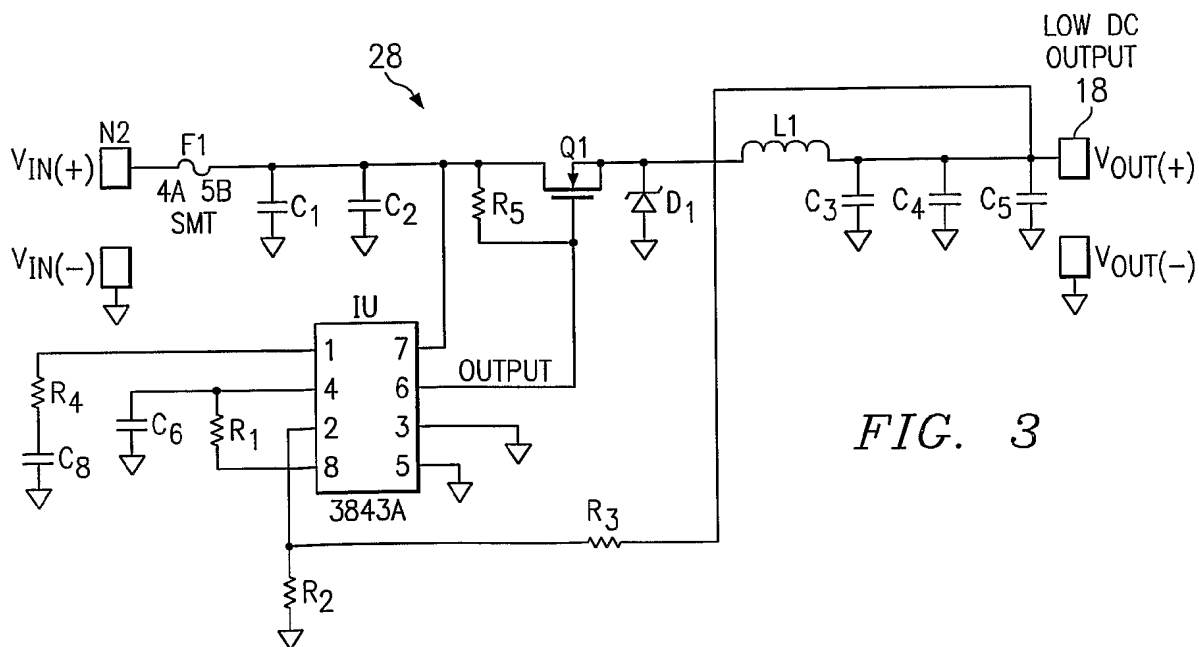
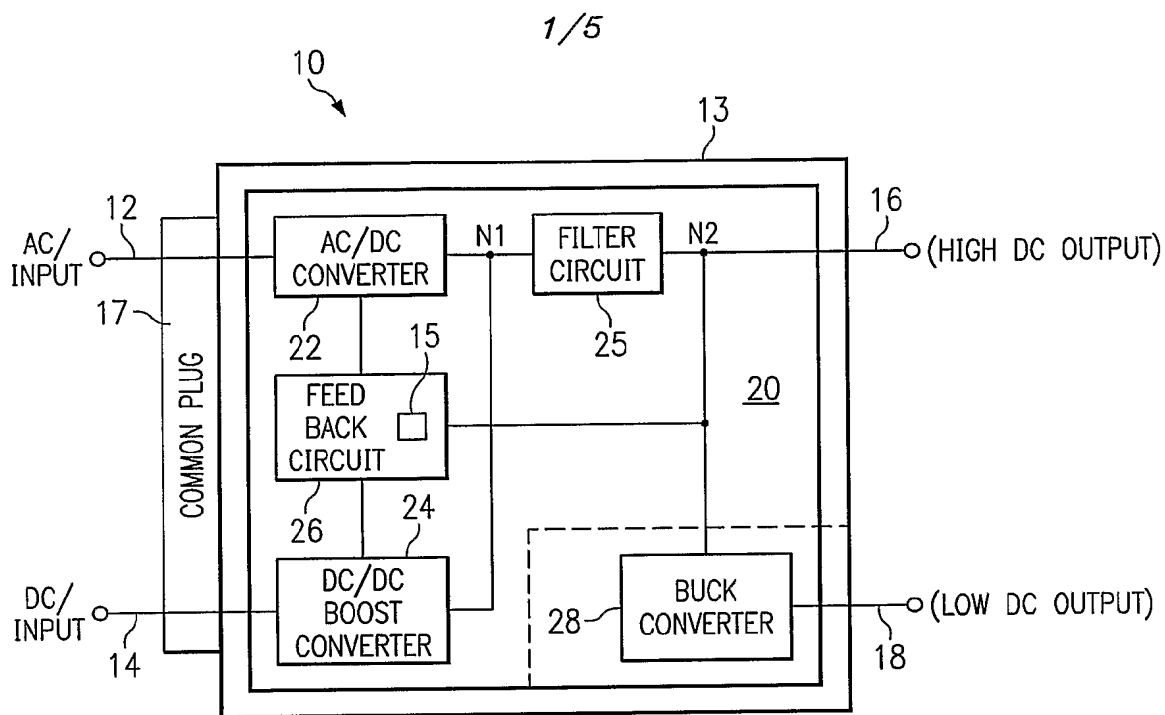
27. The method of Claim 25 wherein said key establishes an output current limiting function.

15

28. The method of Claim 19 wherein said AC input voltage can have a range of 90VAC to 265VAC.

29. The method of Claim 19 wherein said DC input voltage can have a
20 range of 11VDC to 16VDC.

30. A power converter, comprising:
a first circuit converting an AC input voltage to a first
predetermined DC output voltage;
a second circuit converting a DC input voltage to a second
5 predetermined DC output voltage; and
an third circuit receiving said first and second predetermined DC
voltages and, in response thereto, providing a selectable DC output voltage at a
first output, wherein said selectable DC output voltage is established as a function
a removable program module.
- 10
31. The power converter of Claim 30 wherein said first and second
predetermined DC output voltages are substantially the same and are provided to
a common node.
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- 25



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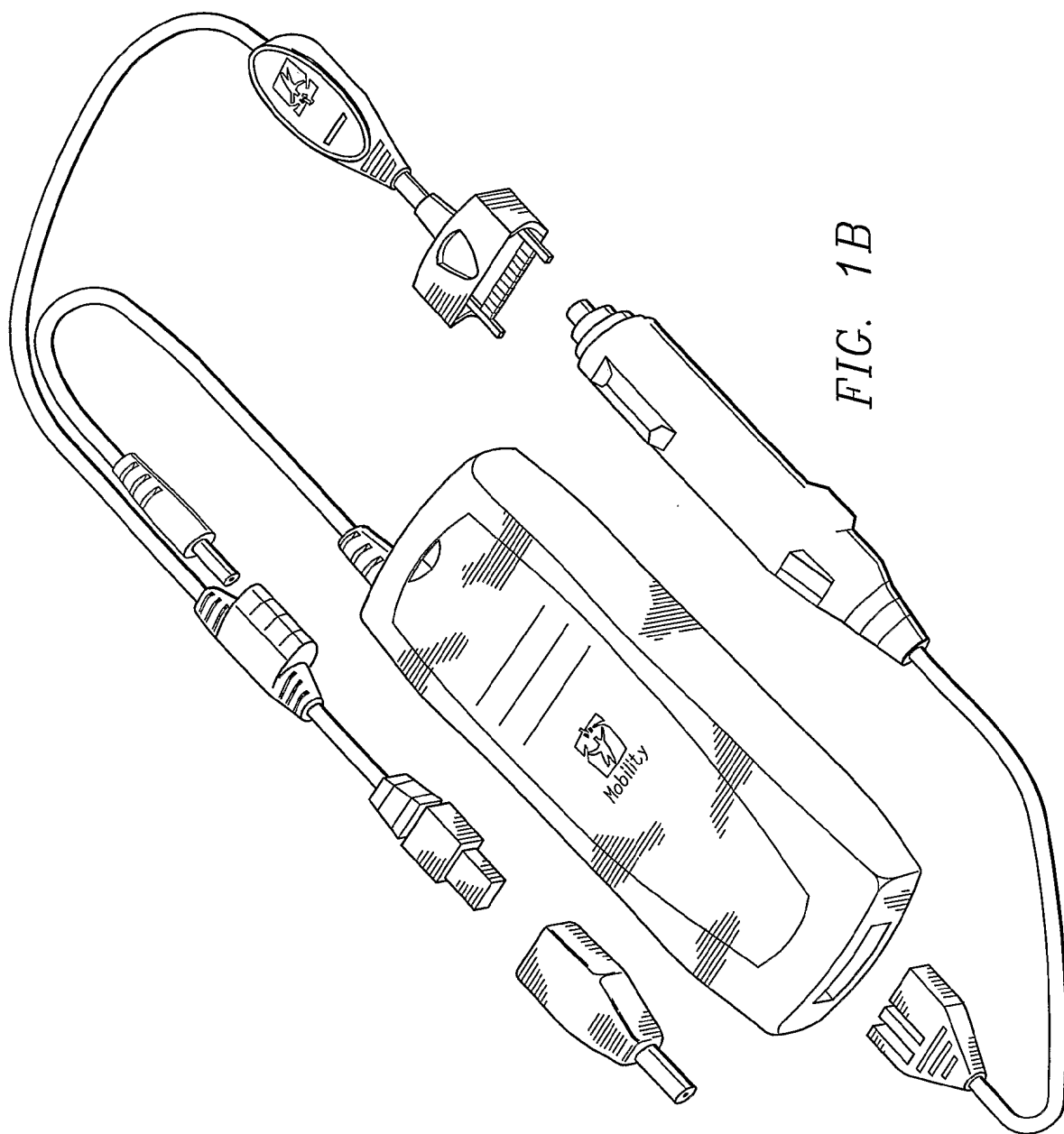
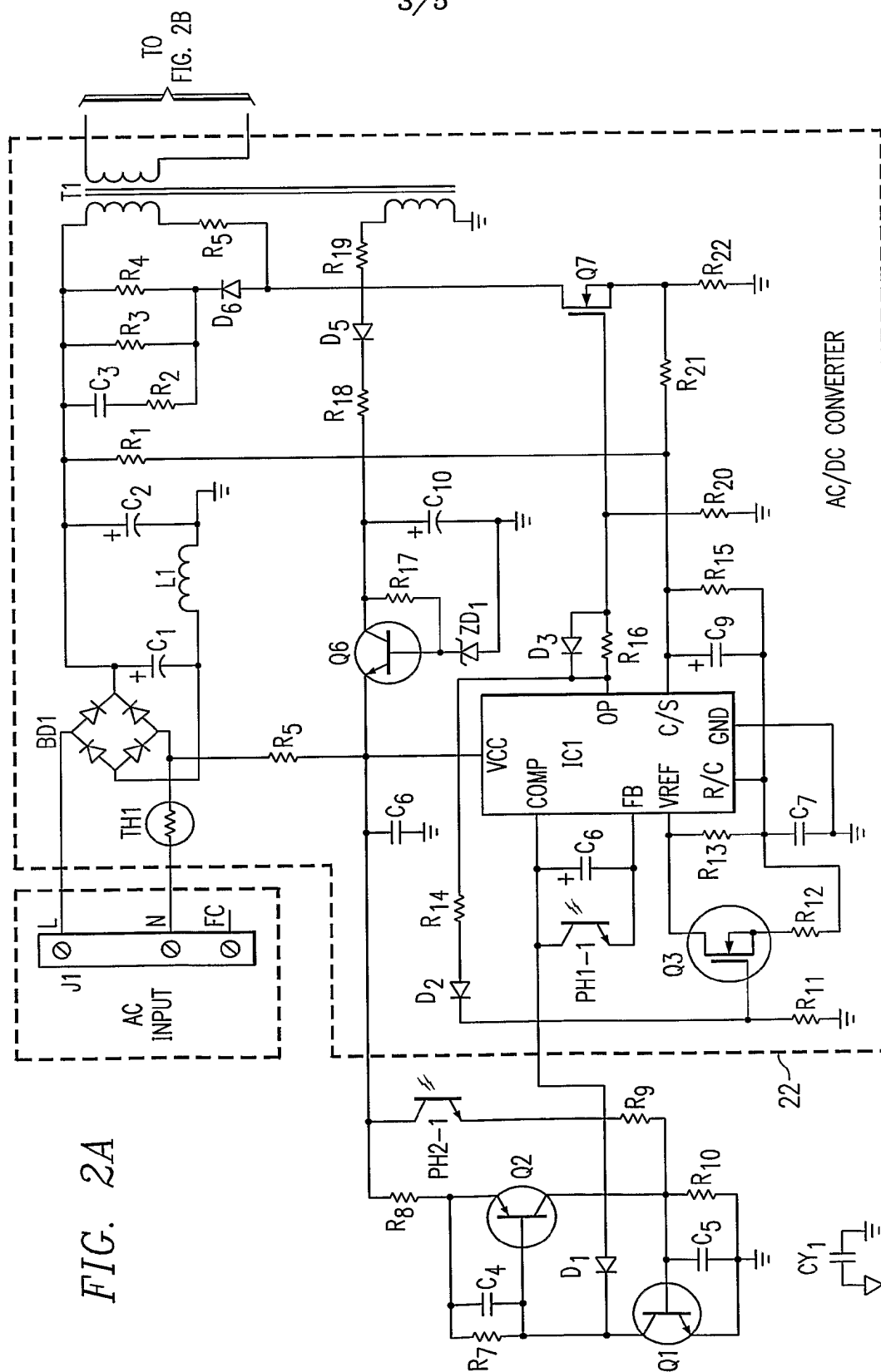
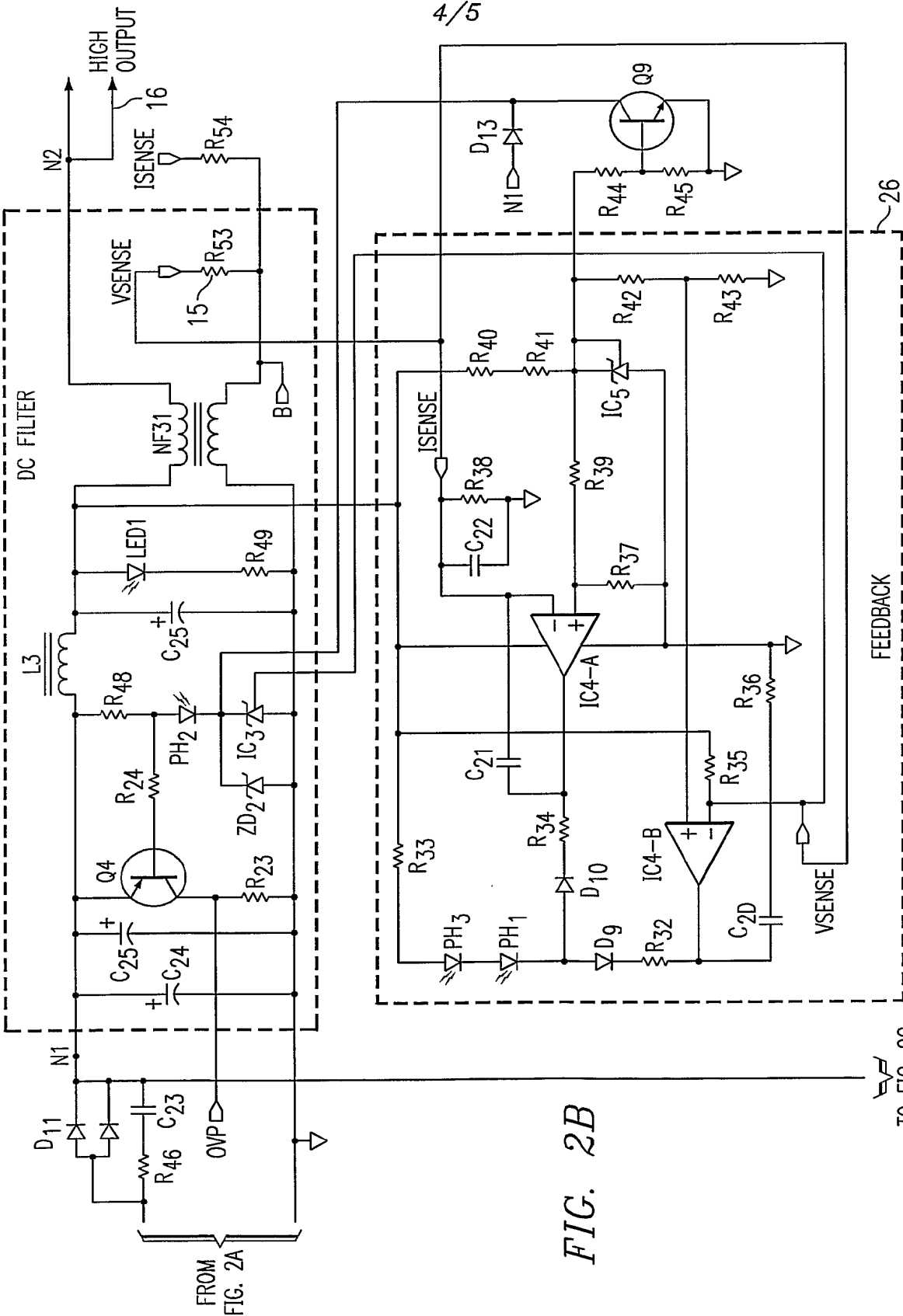
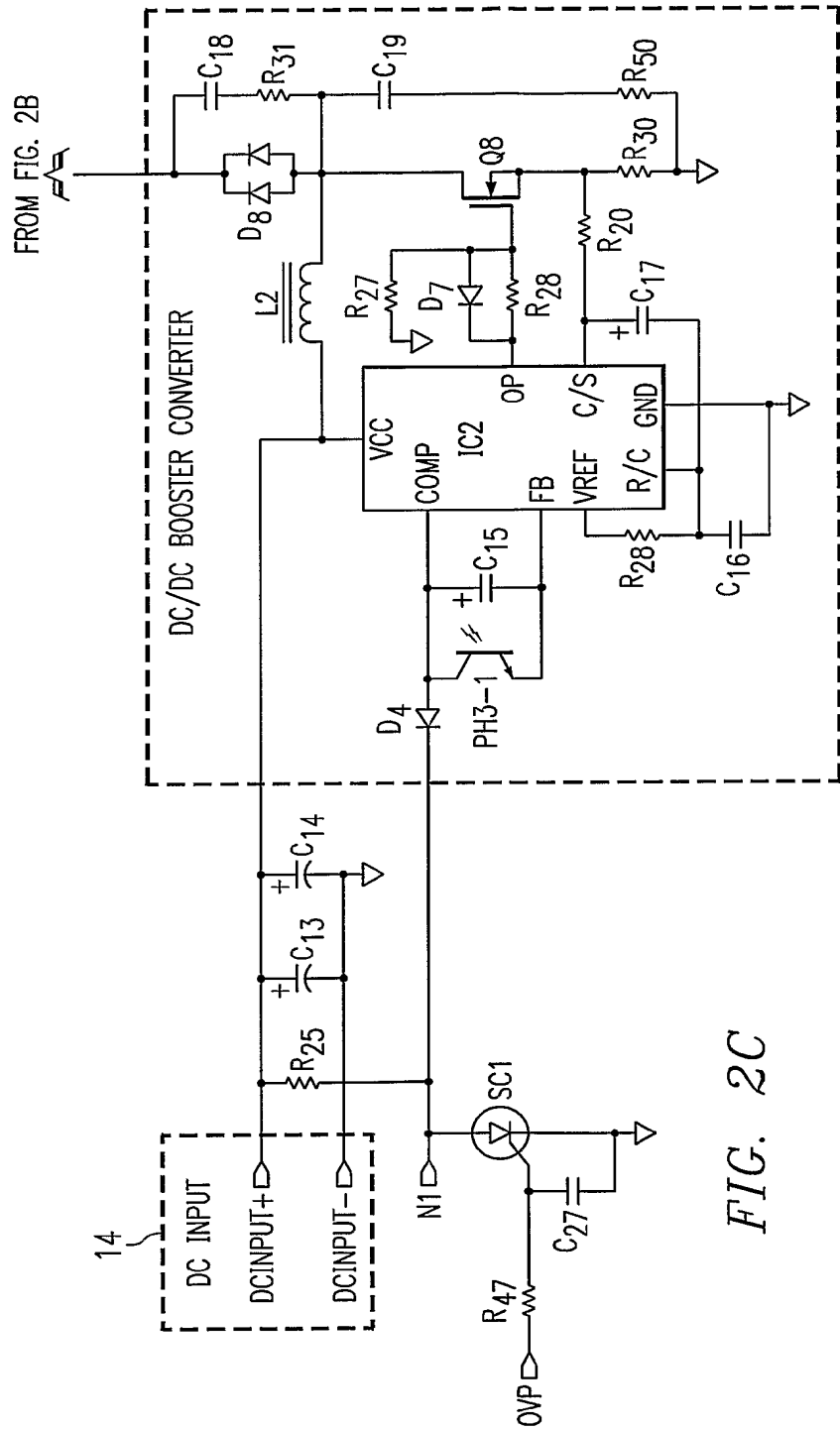


FIG. 1B

FIG. 2A







INTERNATIONAL SEARCH REPORT

International Application No
PCT/US 02/03542

A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 H02M1/10 H02M3/335

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7 H02M H02J

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, PAJ, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	US 5 621 299 A (KRALL DAVID A) 15 April 1997 (1997-04-15)	1-3, 5-15, 19-21, 23-31
A	the whole document ---	4, 16, 22
Y	EP 1 122 873 A (NIHON PROTECTOR CO LTD) 8 August 2001 (2001-08-08)	1-3, 5-15, 19-21, 23-31
	the whole document ---	
A	US 6 137 280 A (ACKERMANN ERIC ET AL) 24 October 2000 (2000-10-24) the whole document ---	1-31
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☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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Date of the actual completion of the international search

28 February 2003

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INTERNATIONAL SEARCH REPORT

Int'l Patent Application No
PCT/US 02/03542

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	COOKE P: "Control loop interactions for secondary side post regulators" APPLIED POWER ELECTRONICS CONFERENCE AND EXPOSITION, 1998. APEC '98. CONFERENCE PROCEEDINGS 1998., THIRTEENTH ANNUAL ANAHEIM, CA, USA 15-19 FEB. 1998, NEW YORK, NY, USA, IEEE, US, 15 February 1998 (1998-02-15), pages 369-376, XP010263620 ISBN: 0-7803-4340-9 the whole document -----	3,7,19, 21,24
A	US 5 901 056 A (HUNG SHENG-CHUAN) 4 May 1999 (1999-05-04) the whole document -----	1,19,30

INTERNATIONAL SEARCH REPORT

International Application No
PCT/US 02/03542

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
US 5621299	A	15-04-1997	NONE	
EP 1122873	A	08-08-2001	JP 3304944 B2	22-07-2002
			JP 2001218455 A	10-08-2001
			EP 1122873 A2	08-08-2001
			US 6504270 B1	07-01-2003
US 6137280	A	24-10-2000	NONE	
US 5901056	A	04-05-1999	NONE	